



LC470EUF

Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	47.0" WUXGA TFT LCD
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BUYER	LGE
MODEL	

SUPPLIER	L&T Display Technology., Ltd.
*MODEL	LC470EUF
SUFFIX	LDC6 (RoHS Verified)

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE	DATE
/		
/		
/		

Please return 1 copy for your confirmation with
your signature and comments.

APPROVED BY	SIGNATURE	DATE
/ Team Leader		
REVIEWED BY		
Y. H Choi / Project Leader		
PREPARED BY		
/ Engineer		

R&D Dept.
L&T Display Technology., Ltd

Ver. 1.0



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RECORD OF REVISIONS

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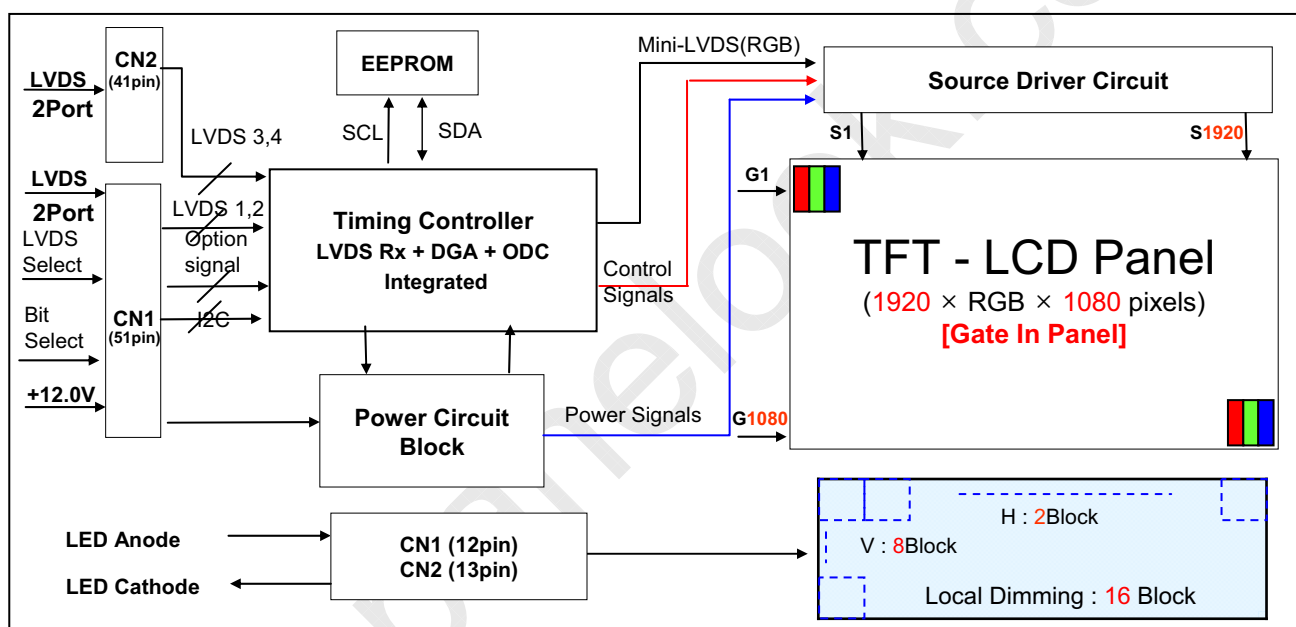
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1. General Description

The LC470EUF is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 46.96inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	46.96 inch (1192.78mm) diagonal
Outline Dimension	1079.0(H) x 625.0 (V) x 10.8(B) / 21.0 mm (D) (Typ.)
Pixel Pitch	0.5415 mm x 0.5415 mm x RGB
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10Bit(D), 1.06 Billion colors
Luminance, White	400 cd/m2 (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 90W(Typ.) [Logic= 8.0W, Backlight=82W (ExtVbr_B=100%)]
Weight	12.4 Kg
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(2H), Anti-glare treatment of the front polarizer (Haze 10%)



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2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or **permanent** damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
			Min	Max		
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	1
	Driver	VBL	-0.3	+ 27.0	VDC	
LED Input Voltage		VF	-	+180.0	VDC	
T-Con Option Selection Voltage		VLOGIC	-0.3	+4.0	VDC	2
Panel Front Temperature		TSUR	-	+68	°C	

Note 1. Ambient temperature condition ($T_a = 25 \pm 2^\circ\text{C}$)

2. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C . The range of operating temperature may be degraded in case of improper thermal management in final product design.

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3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other is used for the LED backlight and LED Driver circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min	Typ	Max		
Circuit :						
Power Input Voltage	V _{LCD}	10.8	12.0	13.2	V _{DC}	
Power Input Current	I _{LCD}	-	670	871	mA	1
		-	970	1261	mA	2
Power Consumption	P _{LCD}	-	8.04	10.5	Watt	1
Rush current	I _{RUSH}	-	-	5.0	A	3

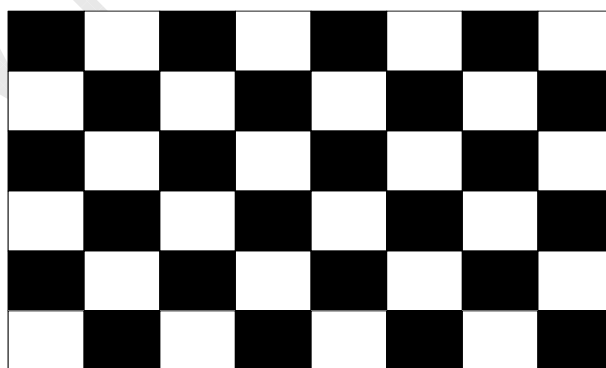
Note 1. The specified current and power consumption are under the $V_{LCD}=12.0V$, $T_a=25 \pm 2^{\circ}C$, $f_v=120Hz$ condition, and mosaic pattern(8 x 6) is displayed and f_v is the frame frequency.

2. The current is specified at the maximum current pattern.

3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

White : 1023 Gray

Black : 0 Gray

**Mosaic Pattern(8 x 6)**

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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol	Values			Unit	Note
			Min	Typ	Max		
Backlight Assembly :							
Forward Current (one array)	Anode	I _{F (anode)}		400		mAdc	±5%
	Cathode	I _{F (cathode)}	95	100	105	mAdc	2, 3
Forward Voltage		V _F	46.4	51.2	56	Vdc	4
Forward Voltage Variation		△V _F			1.7	Vdc	5
Power Consumption		P _{BL}	74.2	81.9	89.6	W	6
Burst Dimming Duty		On duty	10	–	100	%	
Burst Dimming Frequency		1/T	95		182	Hz	8
LED Array : (APPENDIX-V)							
Life Time			30,000	50,000		Hrs	7

Note : The design of the LED driver must have specifications for the LED array in LCD Assembly.

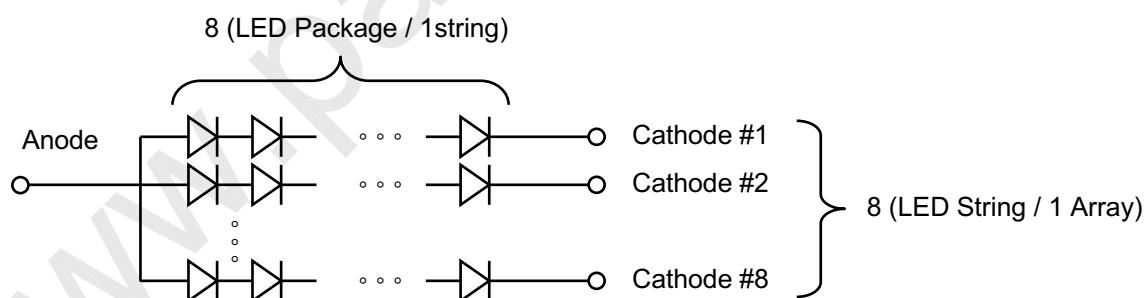
The electrical characteristics of LED driver are based on Constant Current driving type.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD–Assembly should be operated in the same condition as installed in your instrument.

1. Electrical characteristics are based on LED Array specification.
2. Specified values are defined for a Backlight Assembly. (IBL : 2 LED array, 150mA/LED array)
3. Each LED array has 2 anode terminal and 8 cathode terminals.

The forward current(I_F) of the anode terminal is 400mA and it supplies 100mA into 4 strings, respectively



4. The forward voltage (V_F) of LED array depends on ambient temperature (Appendix-V)
5. ΔV_F means Max V_F -Min V_F in one Backlight. So V_F variation in a Backlight isn't over Max. 1.7V
6. Maximum level of power consumption is measured at initial turn on.
Typical level of power consumption is measured after 1hrs aging at $25 \pm 2^\circ\text{C}$.
7. The life time (MTTF) is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at $25 \pm 2^\circ\text{C}$, based on duty 100%.
8. The reference method of burst dimming duty ratio.
It is recommended to use synchronous V-sync frequency to prevent waterfall
($V_{sync} \times 1 = \text{Burst Frequency}$)
Though PWM frequency is over 182Hz (max252Hz), function of backlight is not affected.



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3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector and 41-pin connector are used for the module electronics and 12-pin,13-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF(manufactured by JAE)

Refer to below and next Page table

- Mating Connector : FI-R51HL(JAE) or compatible

Table 4-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC or GND	No Connection (Note 4)	27	Bit Select	'H' or NC= 10bit(D) , 'L' = 8bit
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	R2EN	SECOND LVDS Receiver Signal (E-)
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	R2EP	SECOND LVDS Receiver Signal (E+)
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	R1EN	FIRST LVDS Receiver Signal (E-)	50	VLCD	Power Supply +12.0V
25	R1EP	FIRST LVDS Receiver Signal (E+)	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

- Note
1. All GND(ground) pins should be connected together to the LCD module's metal frame.
 2. All VLCD (power input) pins should be connected together.
 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.
 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
 5. LVDS pin (pin No. **#24,25,40,41**) are used for 10Bit(D) of the LCD module.
If used for 8Bit(R), these pins are no connection.
 6. Specific pin No. **#44** is used for "No signal detection" of system signal interface.
It should be GND for NSB(No Signal Black) during the system interface signal is not.
If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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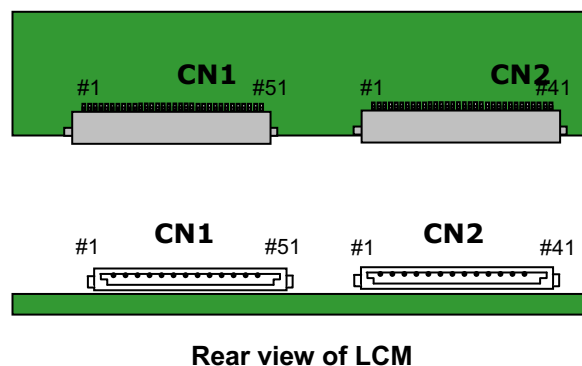
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- LCD Connector (CN2) : FI-RE41S-HF (manufactured by JAE)
- Mating Connector : FI-RE41HL or compatible

Table 4-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC	No connection	22	RE3N	THIRD LVDS Receiver Signal (E-)
2	NC	No connection	23	RE3P	THIRD LVDS Receiver Signal (E+)
3	NC	No connection	24	GND	Ground
4	NC	No connection	25	GND	Ground
5	NC	No connection	26	RA4N	FORTH LVDS Receiver Signal (A-)
6	NC	No connection	27	RA4P	FORTH LVDS Receiver Signal (A+)
7	NC	No connection	28	RB4N	FORTH LVDS Receiver Signal (B-)
8	NC	No connection	29	RB4P	FORTH LVDS Receiver Signal (B+)
9	GND	Ground	30	RC4N	FORTH LVDS Receiver Signal (C-)
10	RA3N	THIRD LVDS Receiver Signal (A-)	31	RC4P	FORTH LVDS Receiver Signal (C+)
11	RA3P	THIRD LVDS Receiver Signal (A+)	32	GND	Ground
12	RB3N	THIRD LVDS Receiver Signal (B-)	33	RCLK4N	FORTH LVDS Receiver Clock Signal(-)
13	RB3P	THIRD LVDS Receiver Signal (B+)	34	RCLK4P	FORTH LVDS Receiver Clock Signal(+)
14	RC3N	THIRD LVDS Receiver Signal (C-)	35	GND	Ground
15	RC3P	THIRD LVDS Receiver Signal (C+)	36	RD4N	FORTH LVDS Receiver Signal (D-)
16	GND	Ground	37	RD4P	FORTH LVDS Receiver Signal (D+)
17	RCLK3N	THIRD LVDS Receiver Clock Signal(-)	38	RE4N	FORTH LVDS Receiver Signal (E-)
18	RCLK3P	THIRD LVDS Receiver Clock Signal(+)	39	RE4P	FORTH LVDS Receiver Signal (E+)
19	GND	Ground	40	GND	Ground
20	RD3N	THIRD LVDS Receiver Signal (D-)	41	GND	Ground
21	RD3P	THIRD LVDS Receiver Signal (D+)	-		

- Note :
1. All GND(ground) pins should be connected together to the LCD module's metal frame.
 2. LVDS pin (**pin No. #22,23,38,39**) are used for 10Bit(D) of the LCD module.
If used for 8Bit(R), these pins are no connection.



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3-2-2. Backlight Module

[CN201]

1) LED Array Assy Connector (Plug)

: 20022HS-13B2(BK) (manufactured by Yeonho) or equivalent

2) Mating Connector (Receptacle)

: 20022WR-13BD (manufactured by Yeonho) or equivalent

[CN202]

1) LED Array Assy Connector (Plug)

: 20022HS-12B2 (manufactured by Yeonho) or equivalent

2) Mating Connector (Receptacle)

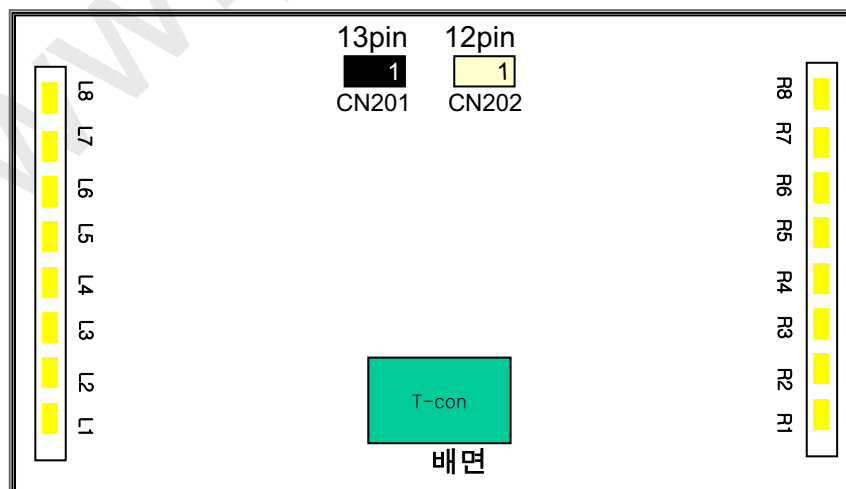
: 20022WR-12BD (manufactured by Yeonho) or equivalent

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201,CN202)

No	Symbol	Description	Note
1	Anode_L1 (1~4Cathode)	LED Input Current	
2	N.C	Open	
3	L1 Cathode	LED Output Current	
4	L2 Cathode	LED Output Current	
5	L3 Cathode	LED Output Current	
6	L4 Cathode	LED Output Current	
7	N.C	Open	
8	L5 Cathode	LED Output Current	
9	L6 Cathode	LED Output Current	
10	L7 Cathode	LED Output Current	
11	L8 Cathode	LED Output Current	
12	N.C	Open	
13	Anode_L2 (5~8Cathode)	LED Input Current	

No	Symbol	Description	Note
1	Anode_R2 (5~8Cathode)	LED Input Current	
2	N.C	Open	
3	R8Cathode	LED Output Current	
4	R7 Cathode	LED Output Current	
5	R6 Cathode	LED Output Current	
6	R5 Cathode	LED Output Current	
7	R4 Cathode	LED Output Current	
8	R3 Cathode	LED Output Current	
9	R2 Cathode	LED Output Current	
10	R1 Cathode	LED Output Current	
11	N.C	Open	
12	Anode_R1 (1~4Cathode)	LED Input Current	

◆ Rear view of LCM





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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	tHV	480	480	480	tCLK	1920 / 4
	Blank	tHB	40	70	200	tCLK	1
	Total	tHP	520	550	680	tCLK	
Vertical	Display Period	tVv	1080	1080	1080	Lines	
	Blank	tVB	20 (228)	45 (270)	86 (300)	Lines	1
	Total	tVP	1100 (1308)	1125 (1350)	1166 (1380)	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	fCLK	66.97	74.25	78.00	MHz	
	Horizontal	fH	121.8	135	140	KHz	2
	Vertical	fV	108 (95)	120 (100)	122 (104)	Hz	2 NTSC: 108~122Hz (PAL : 95~104Hz)

Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode).
If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency

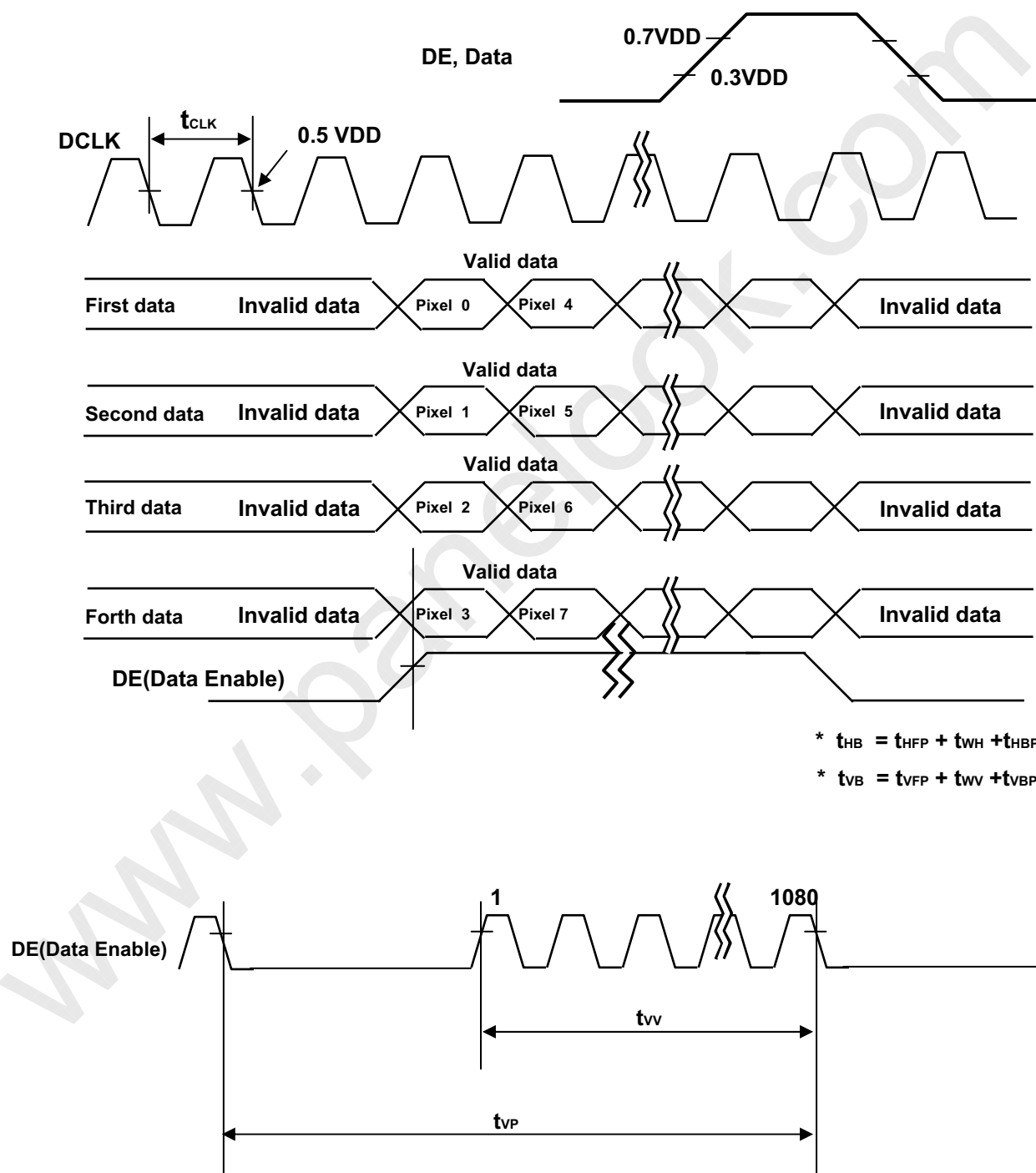
※ Timing should be set based on clock frequency.

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3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram

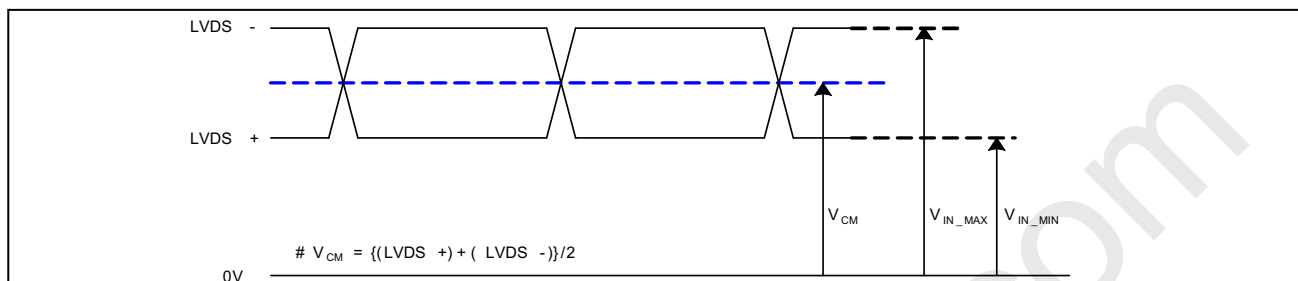


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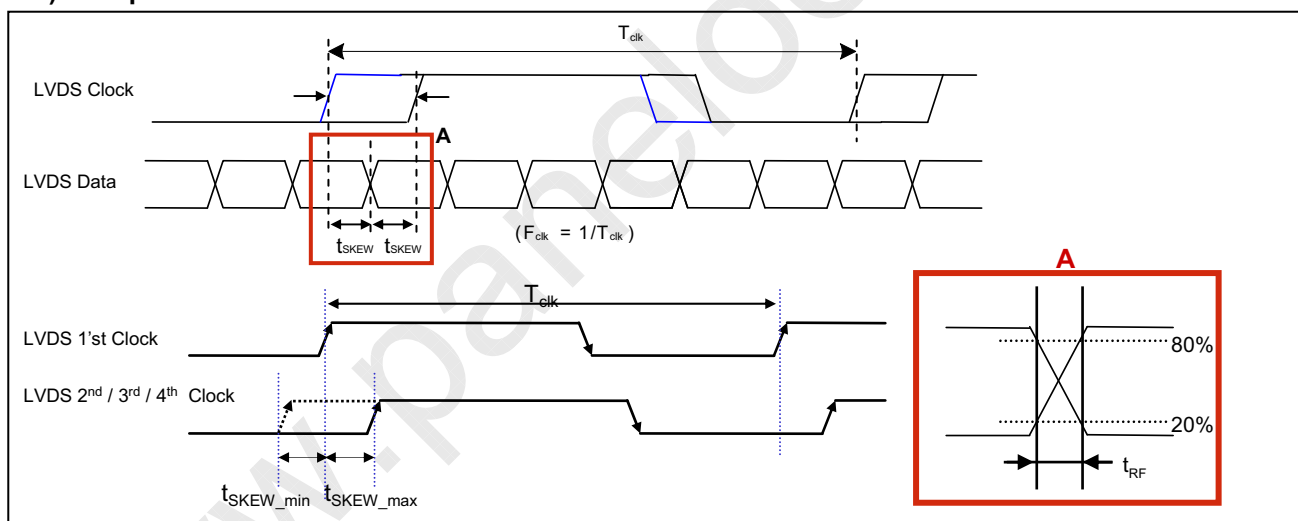
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V_{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V_{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔV_{CM}	-	250	mV	-

2) AC Specification



Description		Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	High Threshold	V _{TH}	100	300	mV	3
	Low Threshold	V _{TL}	-300	-100	mV	
LVDS Clock to Data Skew		t _{SKEW}	-	(0.25*T _{clk}) / 7	ps	-
LVDS Clock/DATA Rising/Falling time		t _{RF}	260	(0.3*T _{clk}) / 7	ps	2
Effective time of LVDS		t _{eff}	± 360	-	ps	-
LVDS Clock to Clock Skew (Even to Odd)		t _{SKEW EO}	-	1/7* T _{clk}	ps	-

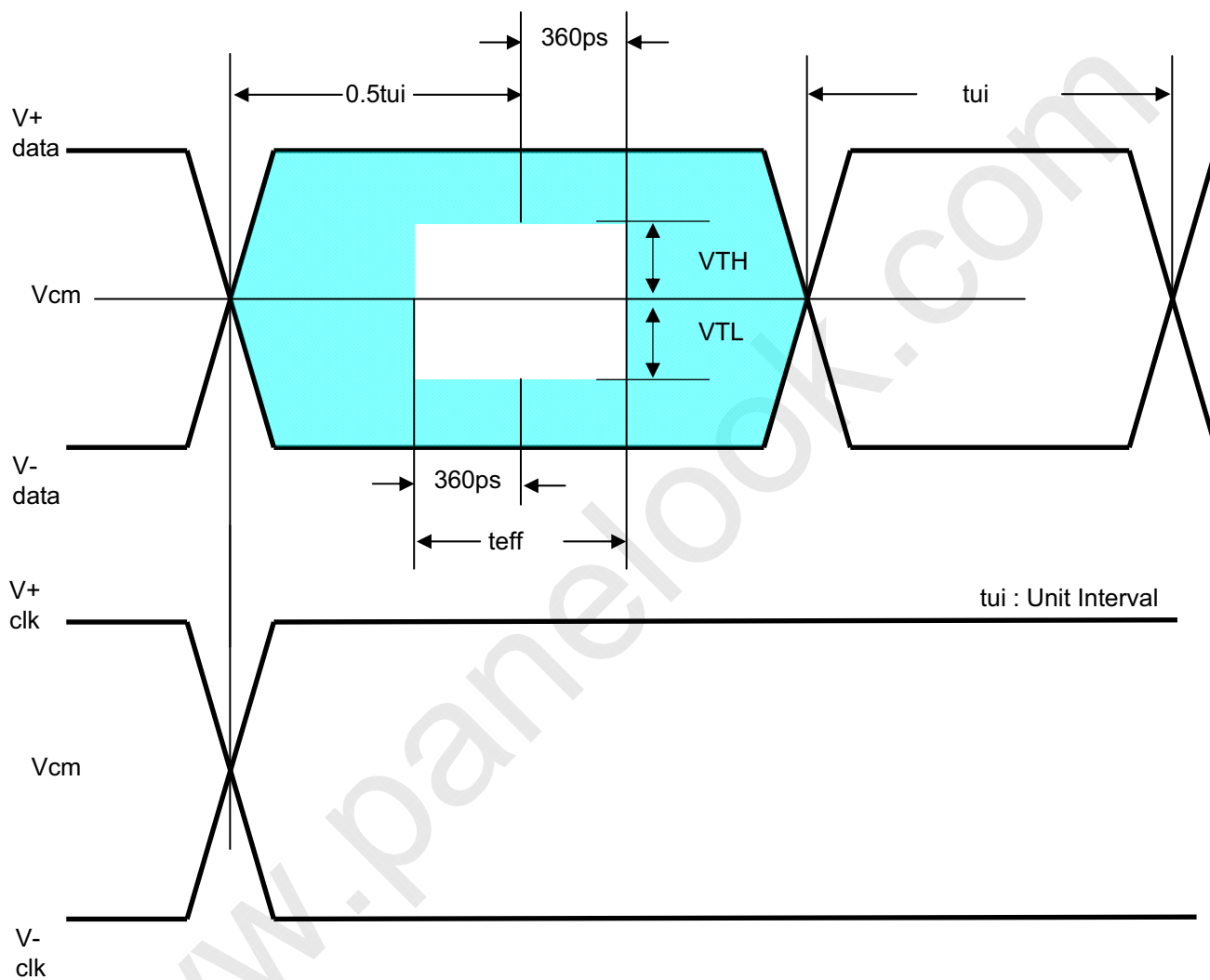
Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. If t_{RF} isn't enough, t_{eff} should meet the range.

3. LVDS Differential Voltage is defined within t_{eff}

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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

Color		Input Color Data																													
		RED										GREEN										BLUE									
		MSB					LSB					MSB					LSB					MSB				LSB					
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (0001)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...										...										...									
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	...	...										...										...									
	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
BLUE	BLUE (0000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (0001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	...	...										...										...									
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

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3-6. Power Sequence

3-6-1. LCD Driving circuit

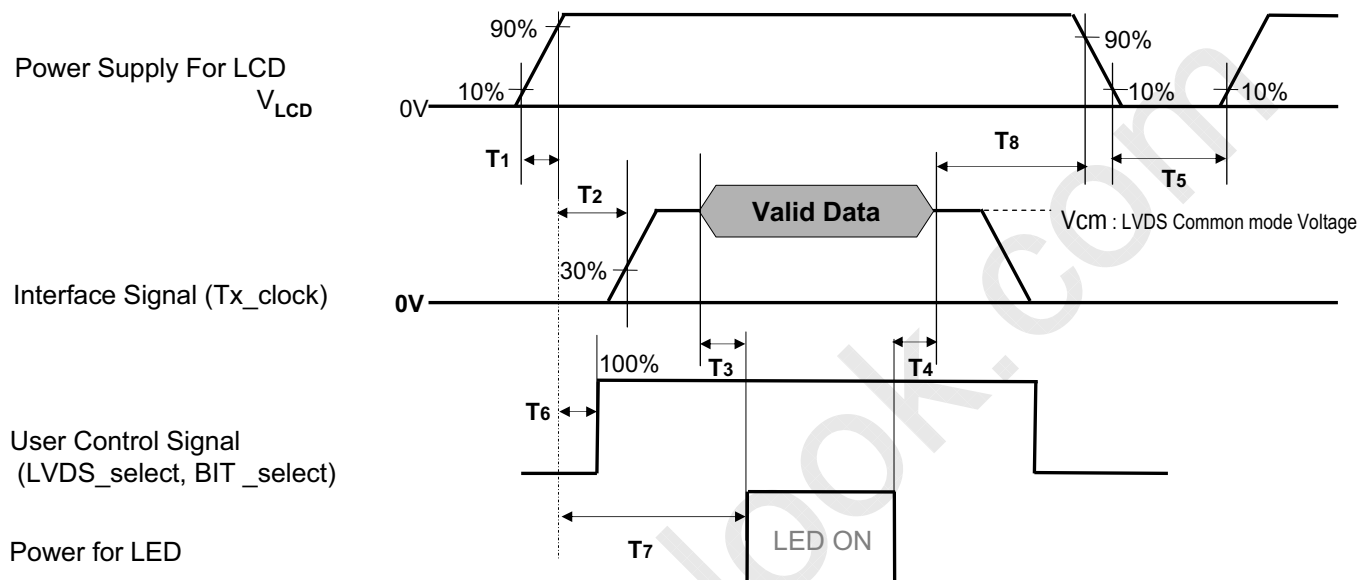


Table 8. POWER SEQUENCE

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
T3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	1.0	-	-	s	4
T6	-	-	T2	ms	5
T7	0.5	-	-	s	6
T8	100	-	-	ms	7

- Note :
1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 4. T5 should be measured after the Module has been fully discharged between power off and on period.
 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
 6. If there is no abnormal display, no problem.
 7. It is recommendation specification that T8 has to be 100ms as a minimum value.
- ※ Please avoid floating state of interface signal at invalid period.
 ※ When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25 \pm 2^\circ\text{C}$. The values are specified at distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° . FIG. 1 shows additional information concerning the measurement equipment and method.

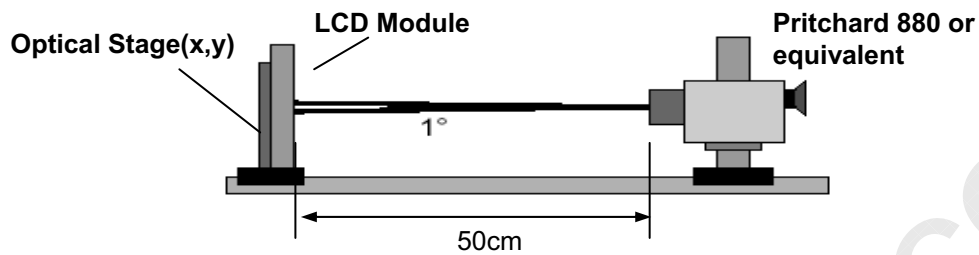


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

$T_a = 25 \pm 2^\circ\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 120\text{Hz}$, $D_{\text{clk}} = 74.25\text{MHz}$
 $I_{\text{F_cathode}} = 100\text{mA}$

Parameter			Symbol		Value			Unit	Note
					Min	Typ	Max		
Contrast Ratio			CR		1100	1600	-		1
Surface Luminance, white			L _{WH}	2D	320	400		cd/m²	2
				3D	120	150			7
Luminance Variation			δ _{WHITE}	5P			1.3		3
Response Time	Gray-to-Gray		G to G		-	5	8	ms	4
	MPRT		MPRT		-	6	9		5
	Uniformity		δ _{MPRT}		-	-	1		5
	Uniformity		δ _{G TO G}		-	-	1		5
Color Coordinates [CIE1931]	RED		Rx	Typ -0.03		0.648	Typ +0.03		
			Ry			0.332			
	GREEN	Gx	0.306						
		Gy	0.606						
	BLUE	Bx	0.150						
		By	0.058						
	WHITE	Wx	0.279						
		Wy	0.292						
Color Temperature						10,000		K	
Color Gamut						72		%	
Viewing Angle	2D (CR>10)	right(φ=0°)	θr (x axis)	89	-	-	degree	6	
		left (φ=180°)	θl (x axis)	89	-	-			
		up (φ=90°)	θu (y axis)	89	-	-			
		down (φ=270°)	θd (y axis)	89	-	-			
	3D (CT≤10%)	up + down	θu (y axis) +θd (y axis)	22	26	-		7	
		up	θu (y axis)	10				7,9	
		down	θd (y axis)	10					
3D Crosstalk			3D C/T			1	3	%	7
Gray Scale					-	-	-		8

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Notes :1. Contrast Ratio(CR) is defined mathematically as :

$$CR = \frac{\text{Surface Luminance at all white pixels}}{\text{Surface Luminance at all black pixels}}$$

It is measured at center 1-point.

2. Surface luminance is determined after the unit has been 'ON' and 1Hour after lighting the backlight in a dark environment at $25 \pm 2^\circ\text{C}$. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
3. The variation in surface luminance , δ WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5})$$
 Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .
 For more information, see the FIG. 2.
4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
 ※ G to G Spec stands for average value of all measured points.
 Photo Detector : RD-80S / Field : 2°
5. MPRT is defined as 10% to 90% blur-edge width Bij(pixels) and scroll speed U(pixels/frame)at the moving picture. For more information, see FIG 4
 ※ Gray to Gray and MPRT Response time uniformity is Reference data. Appendix VIII-1/VIII-2
6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 5.
7. 3D performance specification is expressed by 3D luminance, 3D Crosstalk and 3D viewing angle. 3D luminance and 3D crosstalk is measured at center 1-point. For more information, see the FIG 6~9.
8. Gray scale specification
 Gamma Value is approximately 2.2. For more information, see the Table 11.
9. The Spec. of 3D viewing angle is temporarily permitted by 8° until 4Q of 2011.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ.)
L0	0.06
L63	0.27
L127	1.04
L191	2.49
L255	4.68
L319	7.66
L383	11.5
L447	16.1
L511	21.6
L575	28.1
L639	35.4
L703	43.7
L767	53.0
L831	63.2
L895	74.5
L959	86.7
L1023	100

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Measuring point for surface luminance & measuring point for luminance variation.

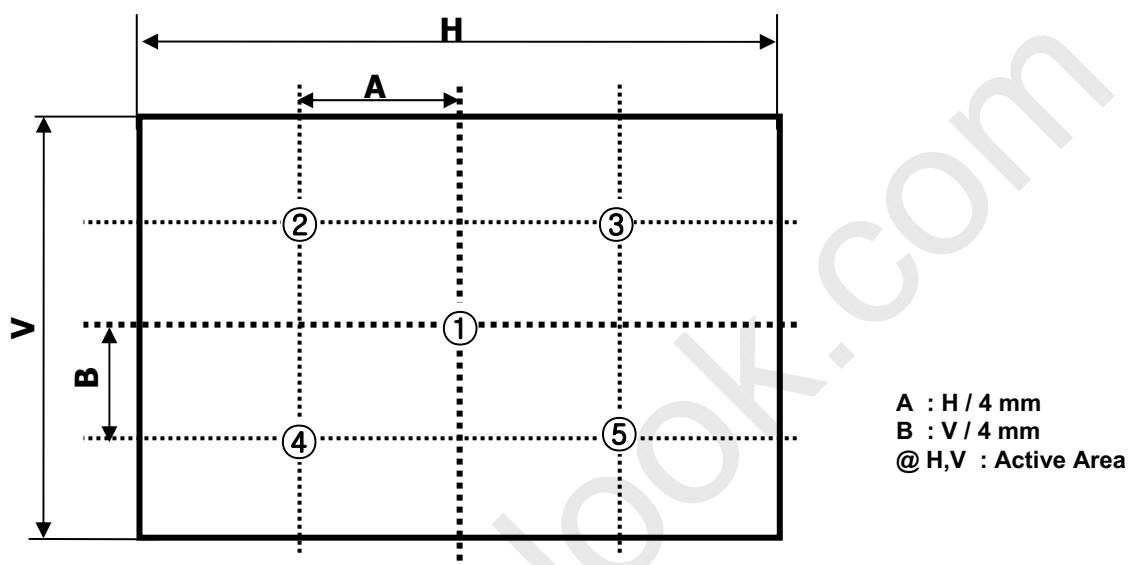


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

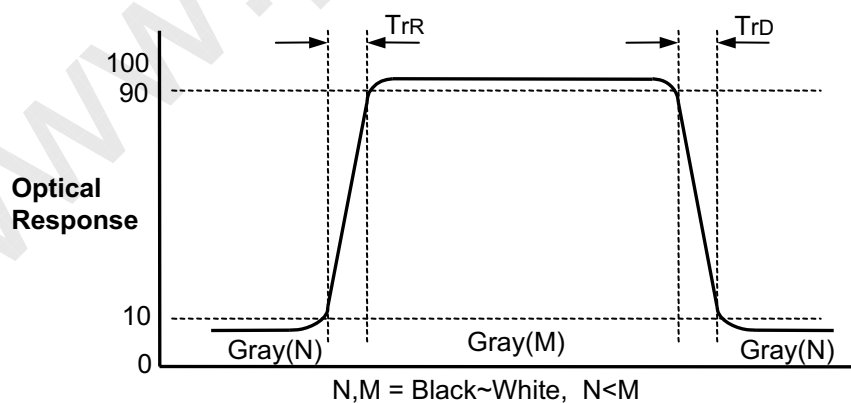


FIG. 3 Response Time

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MPRT is defined as the 10% to 90% blur-edge with B_{ij} (pixels) and scroll speed U (pixels/frame)at the moving picture.

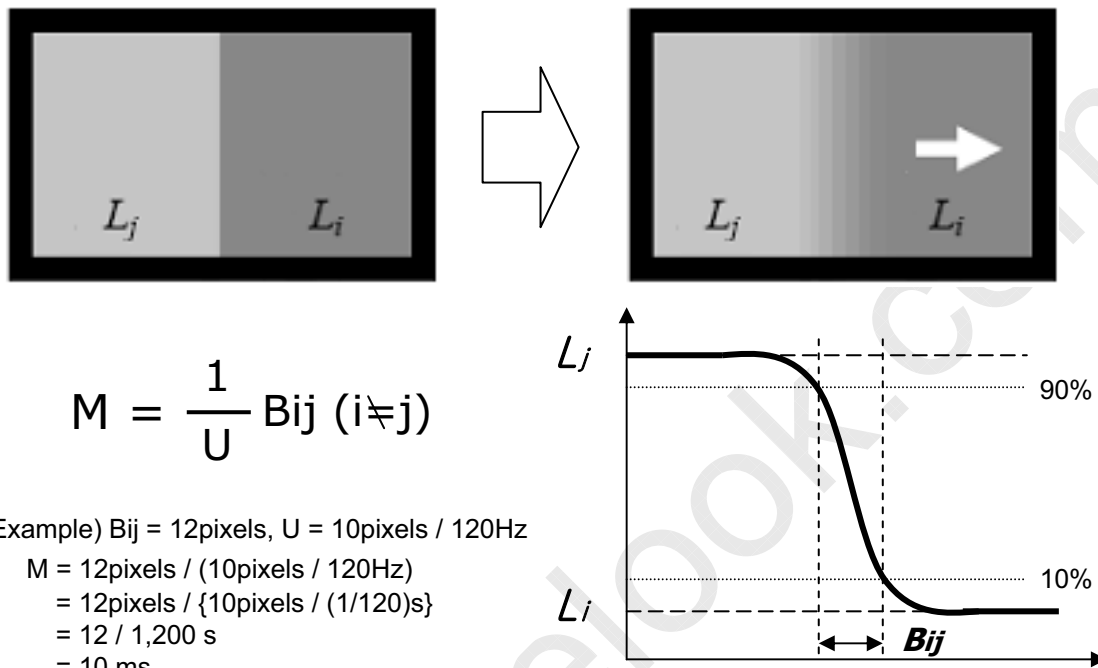


FIG. 4 MPRT

Dimension of viewing angle range

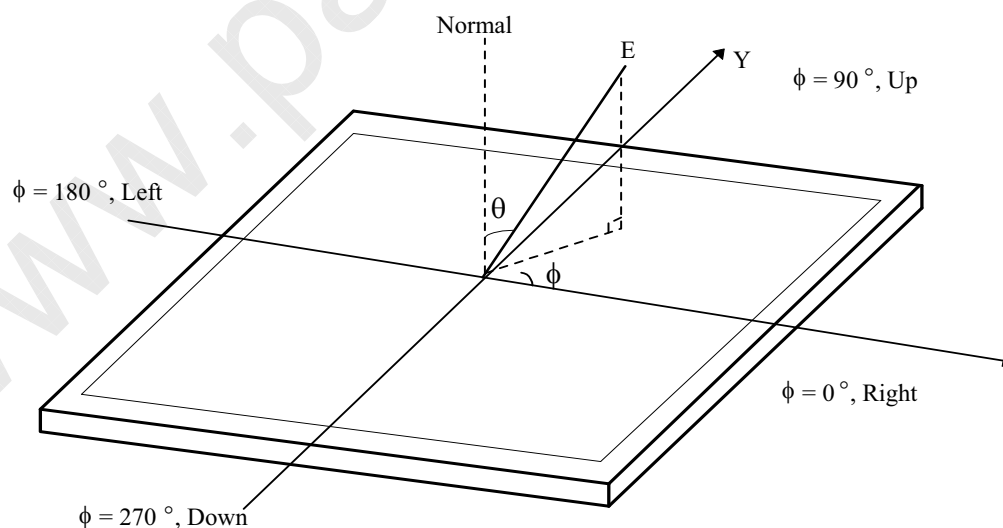
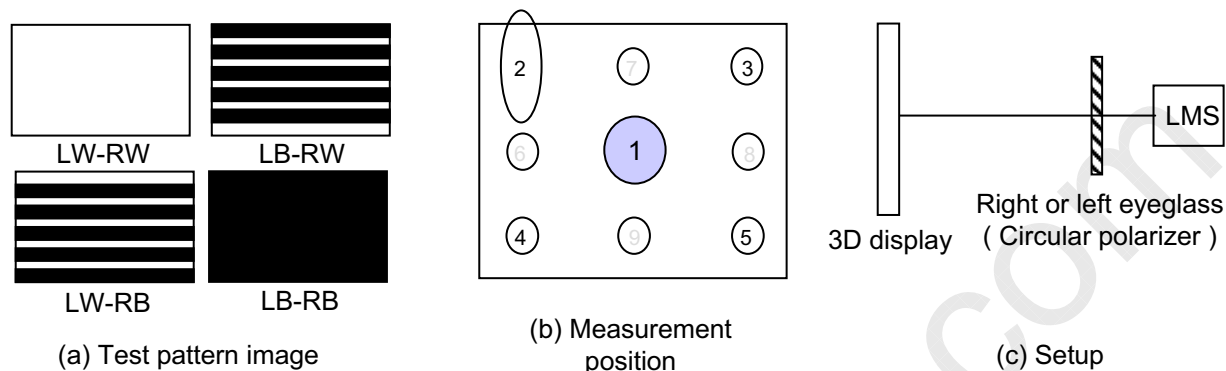


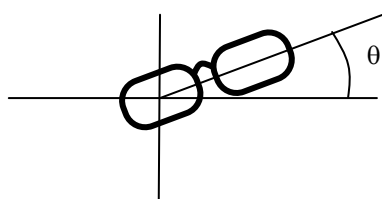
FIG. 5 Viewing Angle

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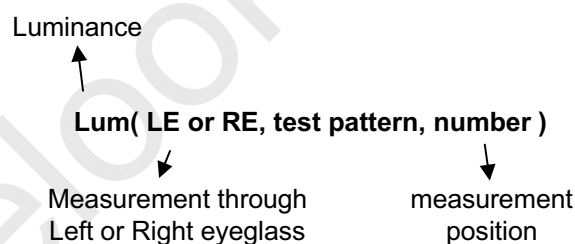
Product Specification



< FIG.6. Measurement configuration >



< FIG.7. Positioning eyeglass >



< FIG. 8. notation of luminance measurement >

In order to measure 3D luminance, 3D crosstalk and 3D viewing angle, it need to be prepared as below;

1) Measurement configuration

4-Test pattern images. Refer to FIG 8.

- LW-RW : White for left and right eye
- LW-RB : White for left eye and Black for right eye
- LB-RW : Black for left eye and white for right eye
- LB-RB : Black for left eye and right eye

Image files where black and white lines are displayed on even or odd lines.

Luminance measurement system (LMS) with narrow FOV (field of view) is used. Refer to FIG 1.

2) Positioning Eyeglass (refer to appendix-VIII for standard specification of eyeglass)

Find angle of minimum transmittance.

This value would be provided beforehand or measured by the following steps;

- (i) Test image (LB-RW) is displayed.
- (ii) Left eyeglass are placed in front of LMS and luminance is measured, rotating right eyeglass such as FIG 7. The notation for luminance measurement is "Lum(LE, LB-RW,1)".
- (iii) Find the angle where luminance is minimum.

* Following measurements should be performed at the angle of minimum transmittance of eyeglass.

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3) Measurement of 3D luminance

- (i) Test image (LW-RW) is displayed.
- (ii) Left or right eyeglass are placed in front of LMS successively and luminance is measured at center 1 point where the notation for luminance measurement is "Lum(LE, LW-RW,1)" or "Lum(RE, LW-RW,1)".

4) Measurement of 3D crosstalk

- (i) Test image (LB-RW, LW-RB and LB-RB) is displayed.
- (ii) Right or left eyeglass are placed in front of LMS successively and luminance is measured for position 1. with rotating LMS or sample vertically.

Average of

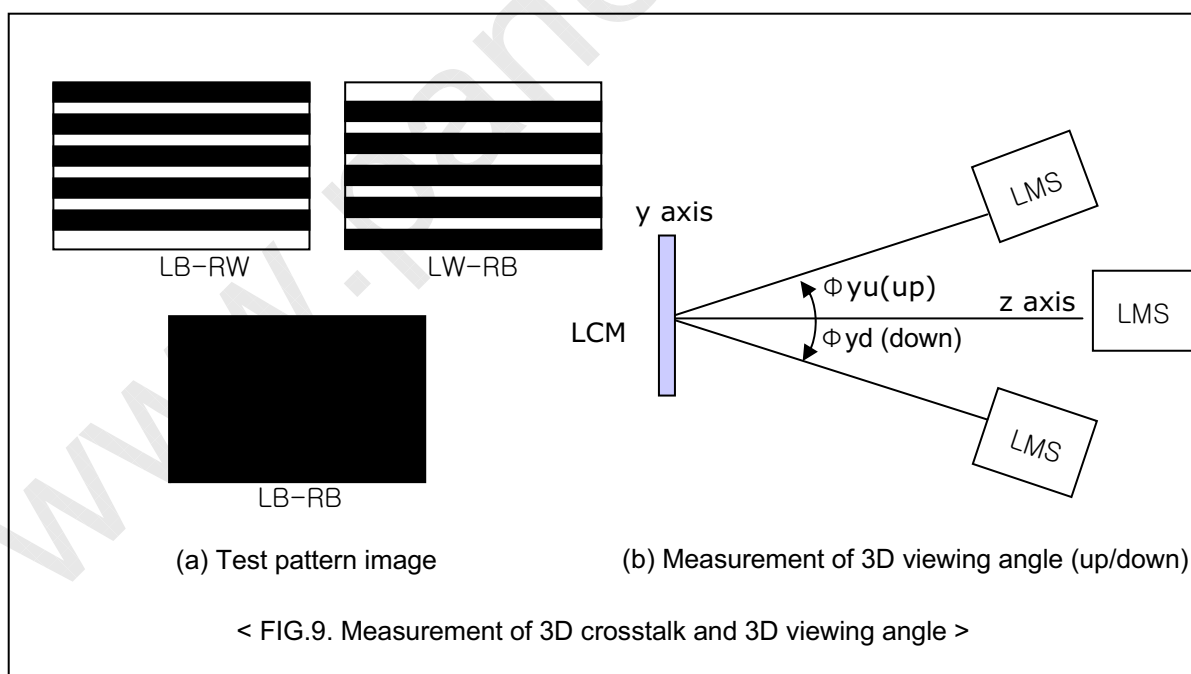
$$\frac{\text{Lum(LE, LB-RW,1)} - \text{Lum(LE, LB-RB,1)}}{\text{Lum(LE, LW-RB,1)} - \text{Lum(LE, LB-RB,1)}}$$

and

$$\frac{\text{Lum(RE, LW-RB,1)} - \text{Lum(RE, LB-RB,1)}}{\text{Lum(RE, LB-RW,1)} - \text{Lum(RE, LB-RB,1)}}$$

5) Measurement of 3D Viewing Angle

3D viewing angle is the angle at which the 3D crosstalk is under 10%. The angles are determined for the vertical or y axis with respect to the z axis which is normal to the LCD module surface and measured for position 1. For more information , see the Fig 9





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5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

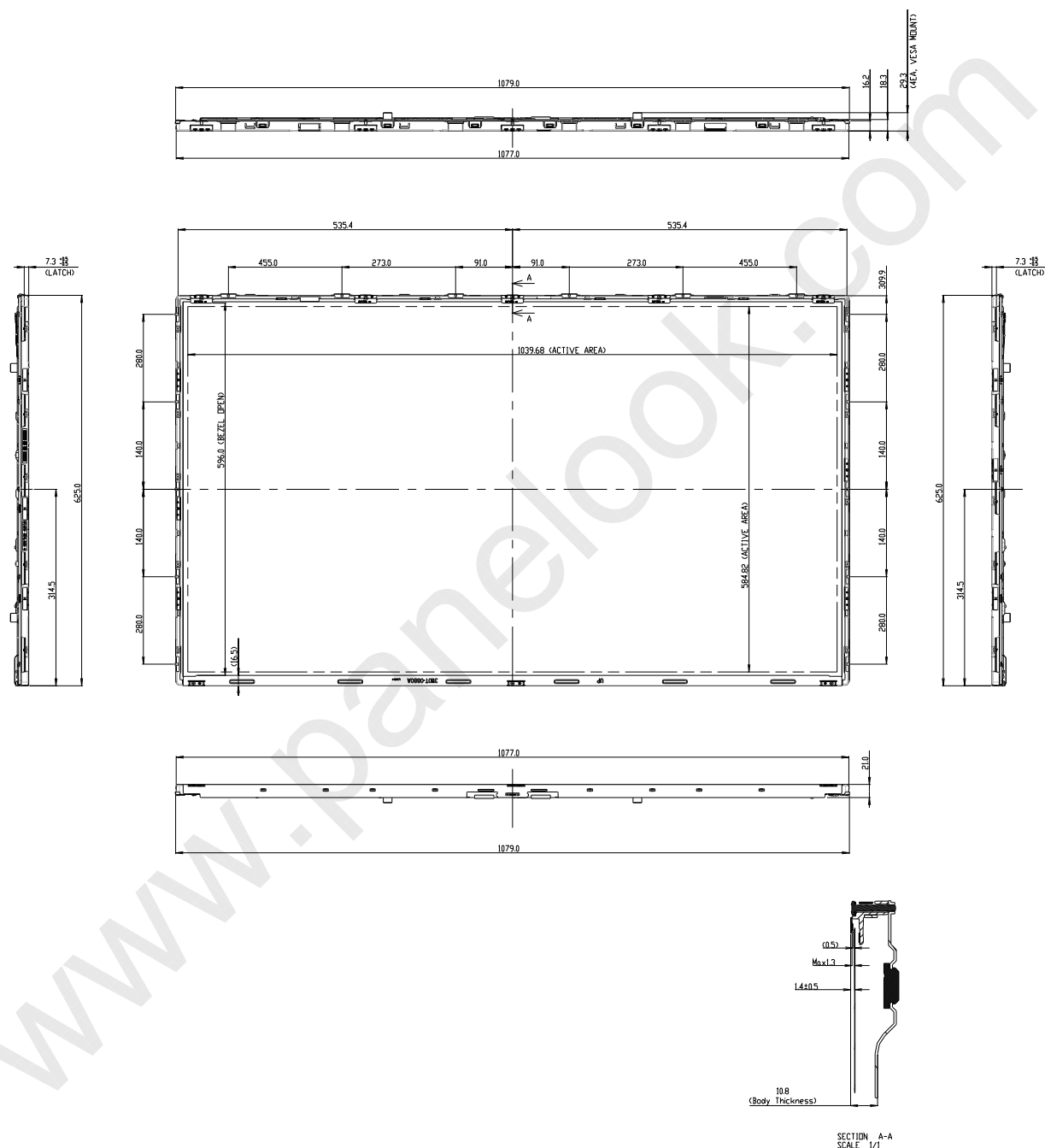
Item	Value	
Outline Dimension	Horizontal	1079.0 mm
	Vertical	625.0 mm
	Depth	10.8 mm (B) / 21.0mm (D)
Bezel Area	Horizontal	N/A
	Vertical	596.0 mm
Active Display Area	Horizontal	1039.68 mm
	Vertical	584.82 mm
Weight	12.4Kg(Typ.), 13.0Kg(Max)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

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<FRONT VIEW>

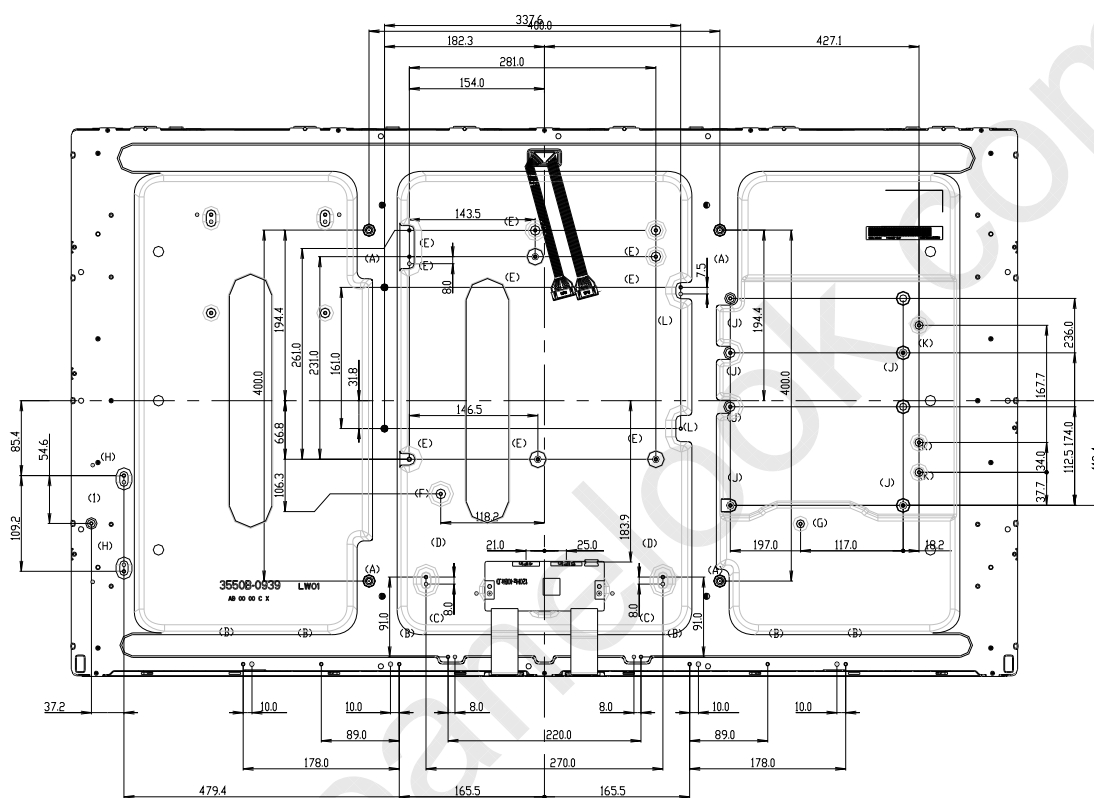




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<REAR VIEW>



NOTES
1. UNSPECIFIED TOLERANCE IS 1.0mm
2. Max. TORQUE : REFER TO TABLE
3. Max. DEPTH : REFER TO TABLE
4. THE SAME FEATURE MEANS THE SAME DIMENSION.
5. DESIGNER'S APPROVAL IS REQUIRED BEFORE MASS-PRODUCTION.

ITEM	TAP	MAX DEPTH (mm)	TORQUE (kgf.cm)	NOTES
(A)	M6	10.0	Max 15.0	4EAK VESA MOUNT)
(B)	M3	5.0	Max 8.0	6EA
(C)	M3	7.5	Max 8.0	2EA
(D)	M3	7.0	Max 8.0	2EA
(E)	M3	4.0	Max 8.0	9EA
(F)	M3	7.0	Max 8.0	1EA
(G)	M3	2.0	Max 8.0	1EA
(H)	M3	4.0	Max 8.0	2EA
(I)	M3	5.0	Max 8.0	1EA
(J)	M3	9.0	Max 8.0	6EA
(K)	M3	4.0	Max 8.0	3EA
(L)	M3	7.0	Max 8.0	2EA

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6. International Standards**6-1. Environment**

- a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



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7. Precautions

Please pay attention to the followings when you use this TFT LCD module.

7-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

7-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
(if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



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7-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

7-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

7-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition

7-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

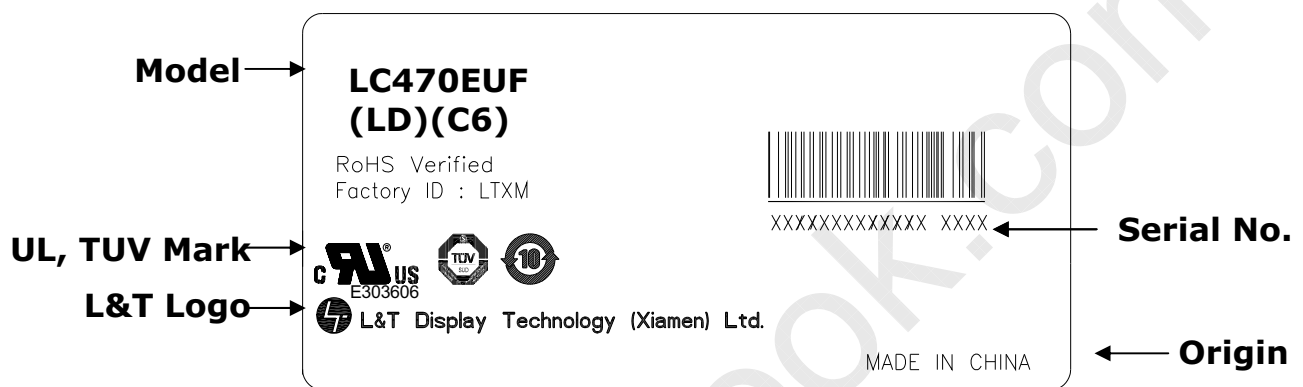
7-7. Operating condition guide

- (1) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 5 ~ 40 °C
 - Display pattern : continually changing pattern (Not stationary)
- (2) If the product will be used in extreme conditions such as high temperature, display patterns or operation time etc.,
It is strongly recommended to contact LGD for Qualification engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems. The LCD product should be applied by global standard environment. (refer ETSI EN 300, IEC 60721)

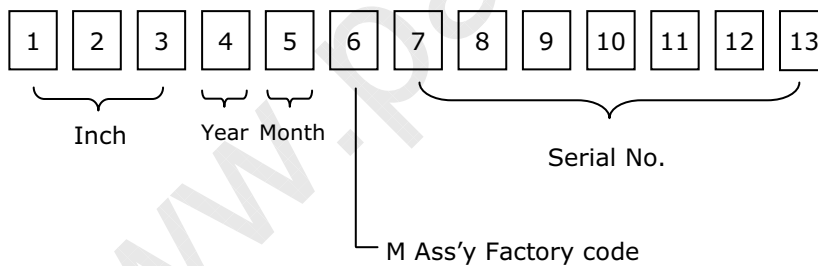
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APPENDIX- II-1

■ LC470EUF-LDC6-LCM Label



■ Serial No.

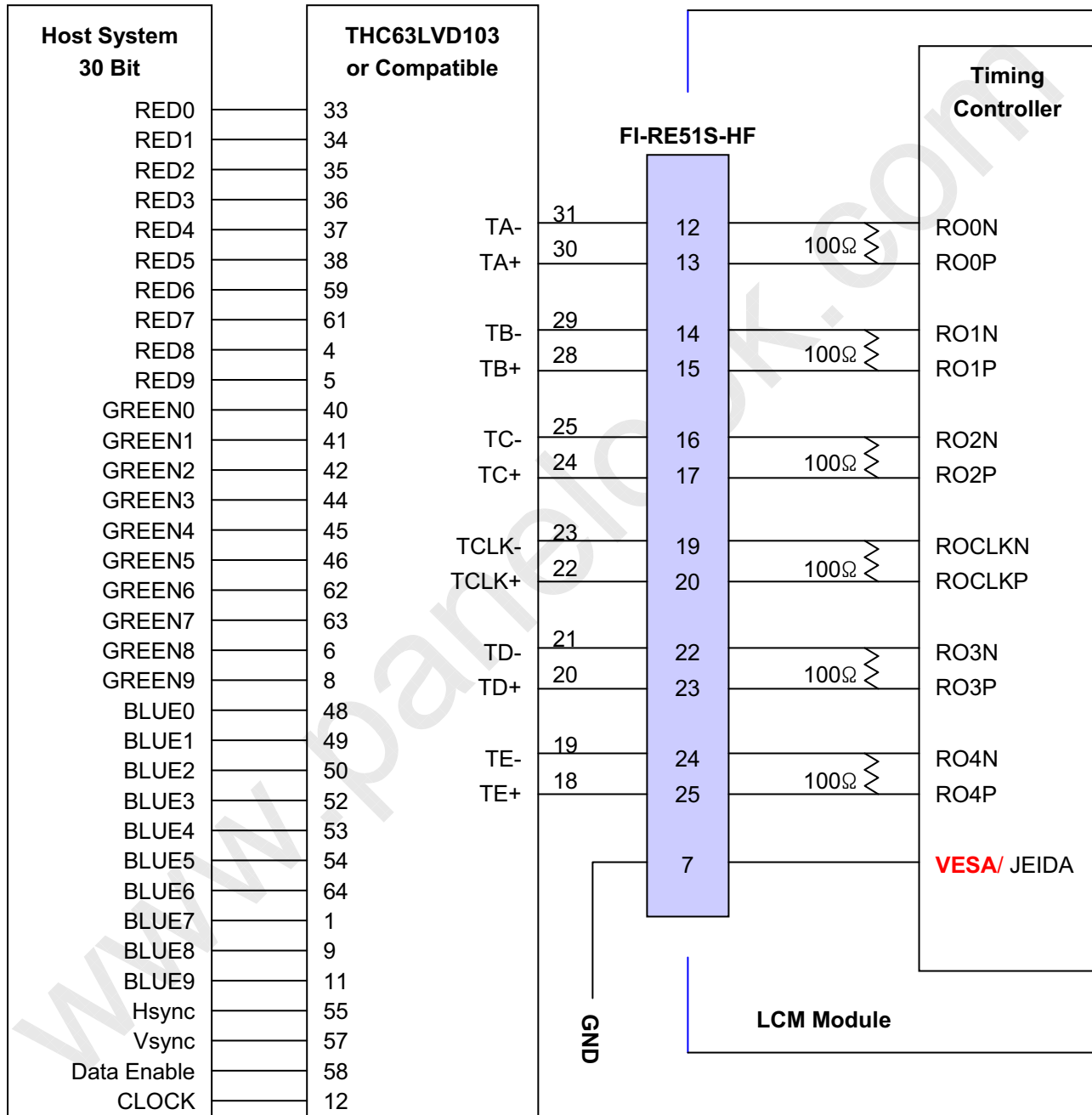


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Product Specification

APPENDIX- II-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= “L” or “NC”)



Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

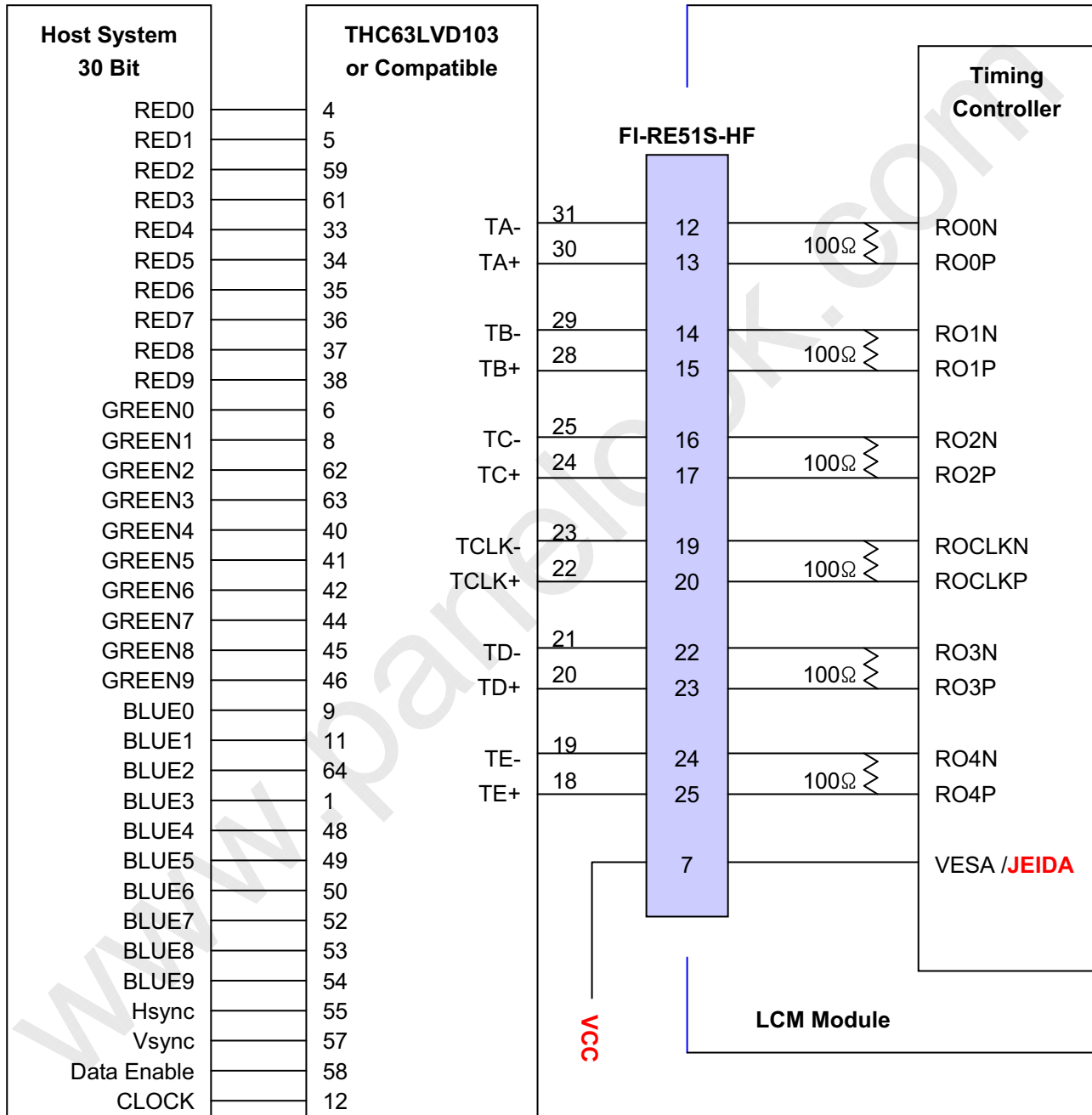
3. '9' means MSB and '0' means LSB at R,G,B pixel data.

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Product Specification

APPENDIX- II-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= “H”)



Note :1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

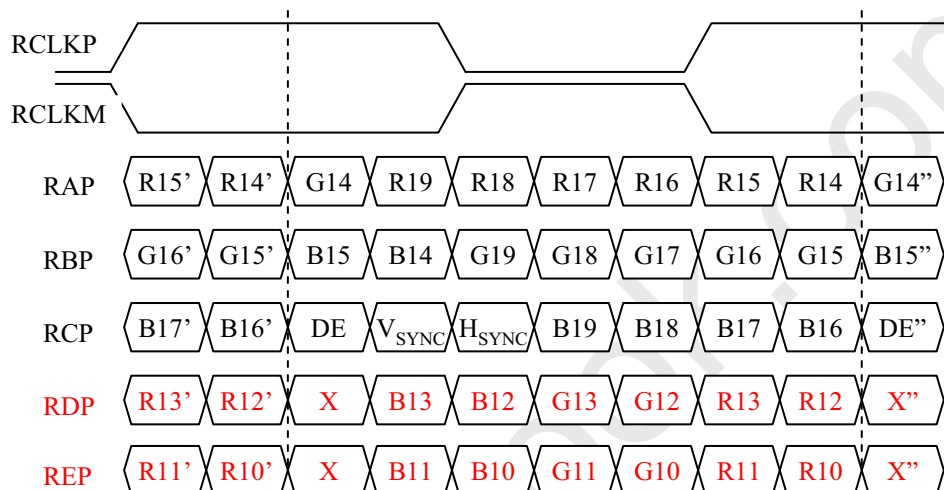
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Product Specification

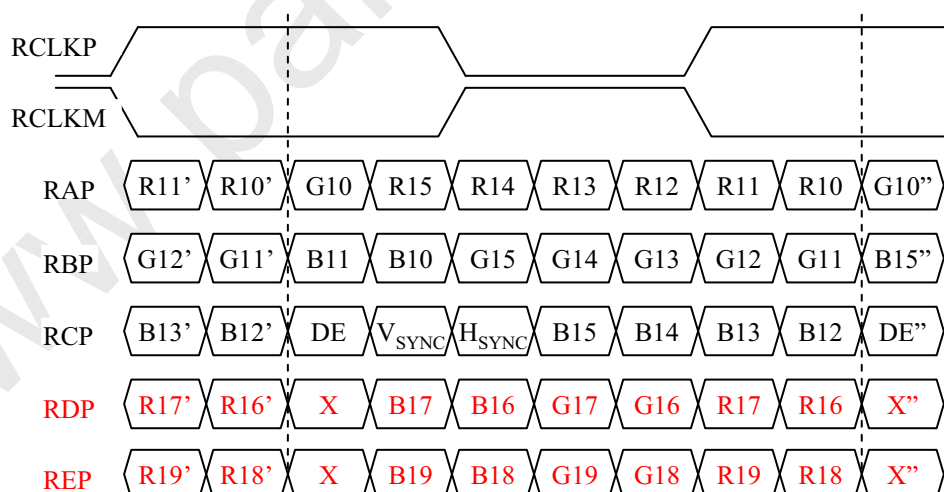
APPENDIX- III-1

■ LVDS Data-Mapping Information (10 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)



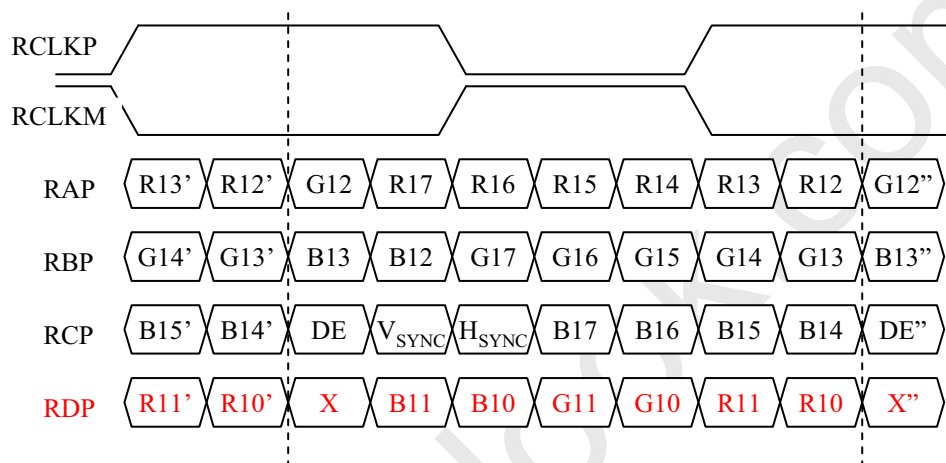
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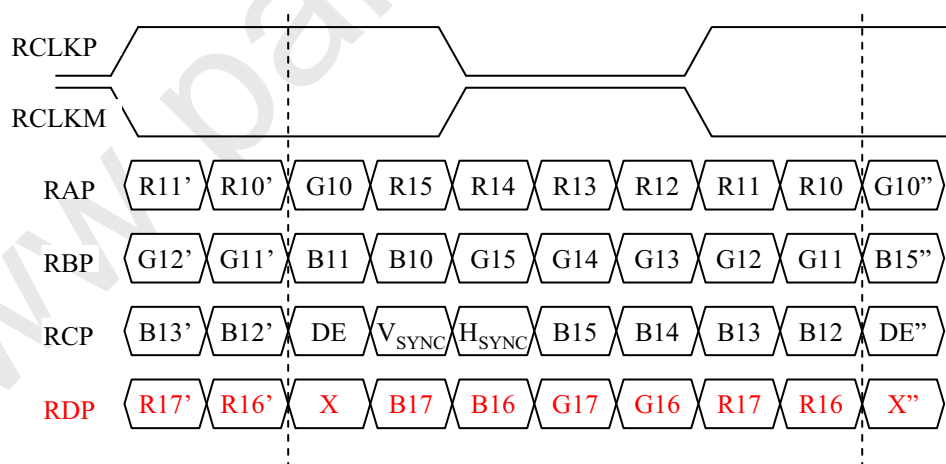
APPENDIX- III-2

■ LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)

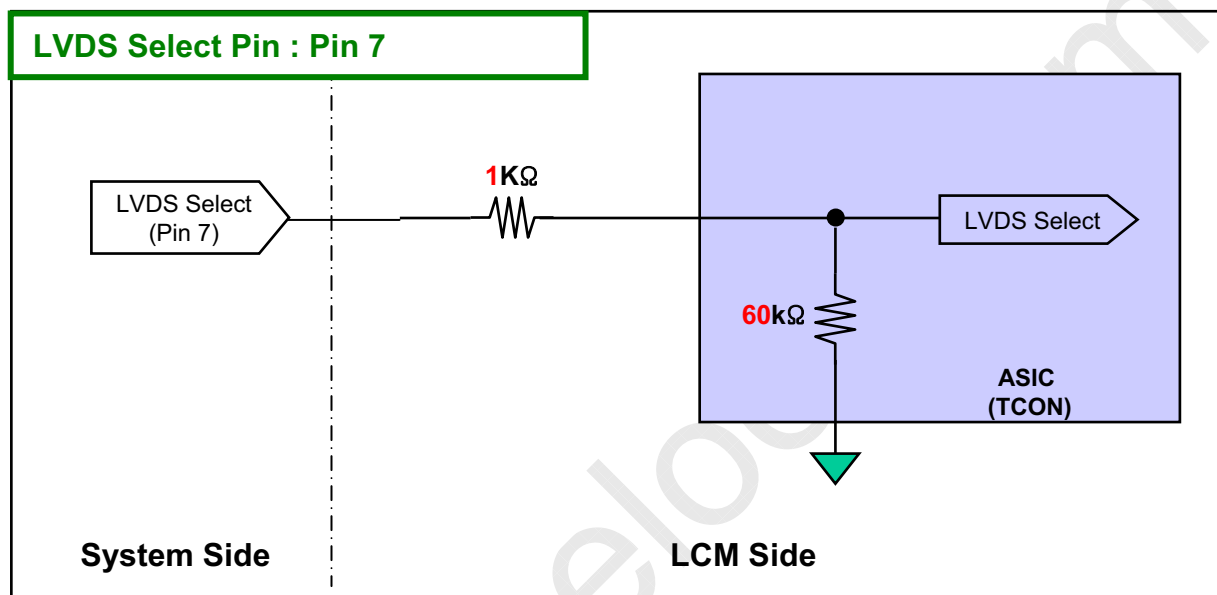
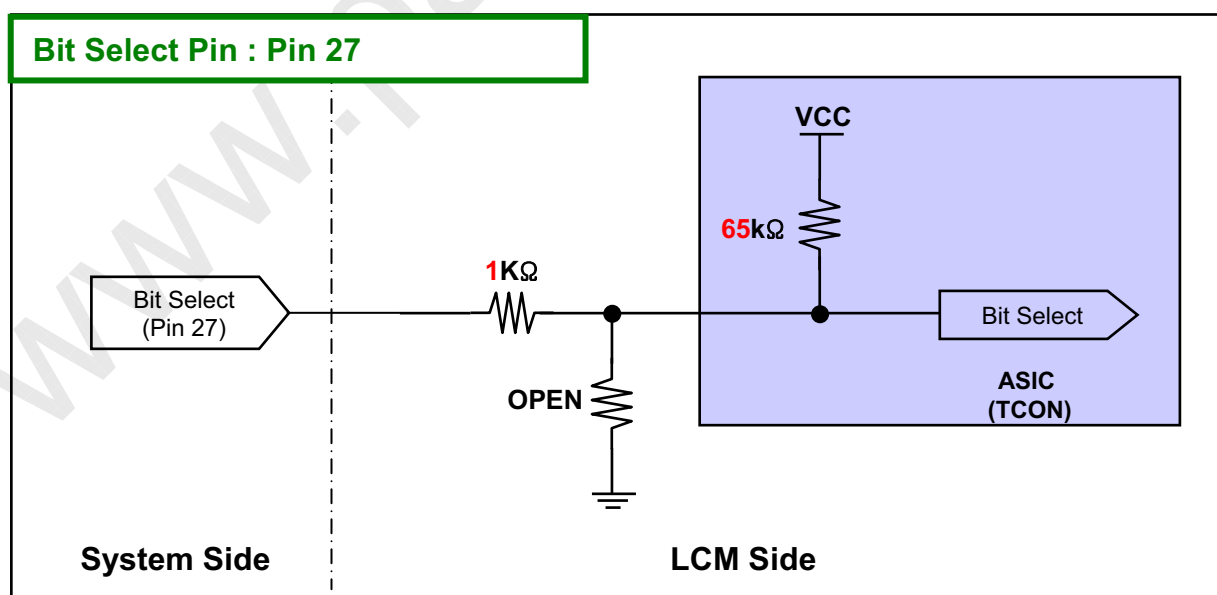


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Product Specification

APPENDIX- IV

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of **LVDS Format** Selection pin2) Circuit Block Diagram of **Bit** Selection pin

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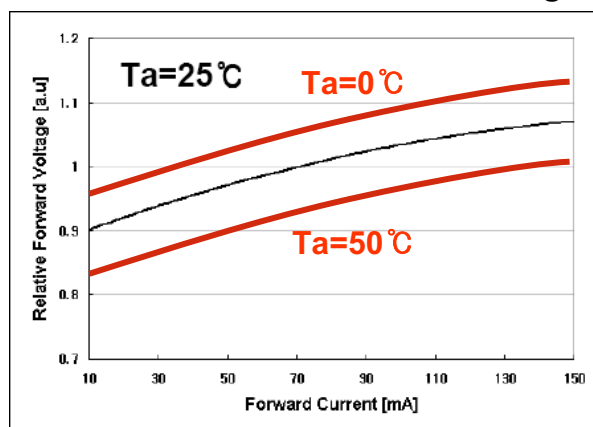
Product Specification

APPENDIX- V

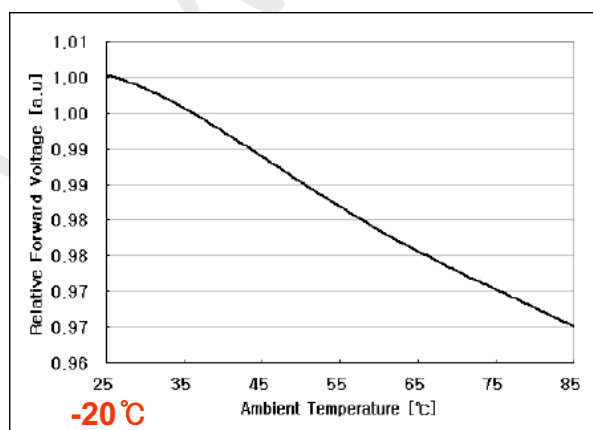
■ LED Array Electrical Spec

Items	Symbol	Condition	Min	Typ	Max	Unit
Module Current	I_F		-	150	240	mA
Array Operating Voltage	V_F	$I_{FM}=150\text{mA}$	126	127	142.8	V
	$\Delta V_{op}^{*2)}$	$I_{FM}=150\text{mA}$			1.3	V

■ Forward Current vs. Forward Voltage



■ Ambient Temperature vs. Forward Voltage

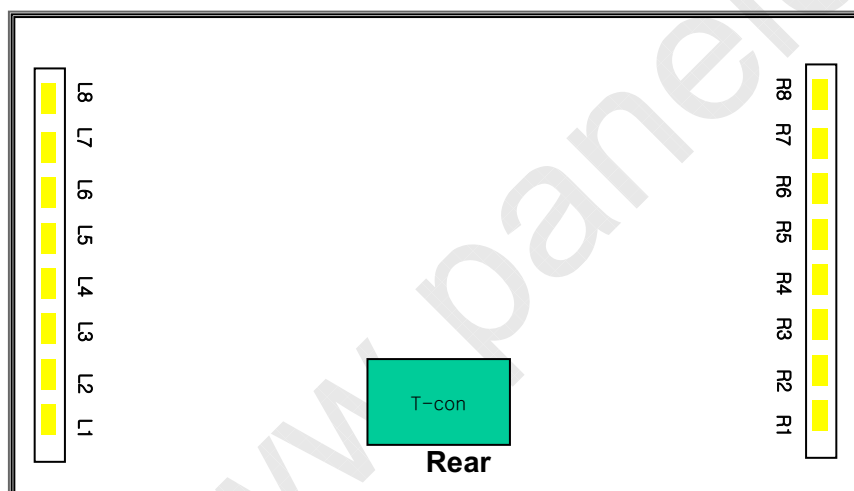
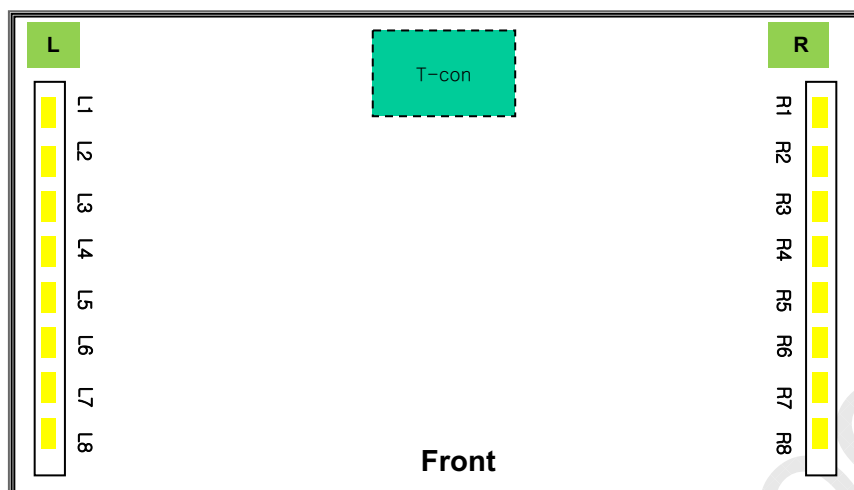


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Product Specification

APPENDIX- VI

■ Local Dimming Block Pin Matching



LED Driver CNT		
Pin No	CN_201	CN_202
1	Anode_L1 (1~4Cathode)	Anode_R2 (5~8Cathode)
2	N.C	N.C
3	L1 Cathode	R8Cathode
4	L2 Cathode	R7 Cathode
5	L3 Cathode	R6 Cathode
6	L4 Cathode	R5 Cathode
7	N.C	R4 Cathode
8	L5 Cathode	R3 Cathode
9	L6 Cathode	R2 Cathode
10	L7 Cathode	R1 Cathode
11	L8 Cathode	N.C
12	N.C	Anode_R1 (1~4Cathode)
13	Anode_L2 (5~8Cathode)	-

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Product Specification

APPENDIX- VII-1

Gray to Gray Response Time Uniformity

This is only the reference data of G to G and uniformity for **LC470EUF-LDC6** model.

1. G to G Response Time :

Response time is defined as Figure3 and shall be measured by switching the input signal for "Gray (N) " and "Gray(M)".(32Gray Step at 8bit)

2. G to G Uniformity

The variation of G to G Uniformity , $\delta_{G \text{ to } G}$ is defined as :

$$G \text{ to } G \text{ Uniformity} = \frac{\text{Maximum}(G \text{ to } G) - \text{Typical}(G \text{ to } G)}{\text{Typical}(G \text{ to } G)} \leq 1$$

*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).

	0Gray	127ray	255Gray	...	895Gray	1023Gray
0Gray		TrR:0G→127G	TrR:0G→255G	...	TrR:0G→895G	TrR:0G→1023G
127Gray	TrD:127G→0G		TrR:127G→255G	...	TrR:127G→895G	TrR:127G→1023G
255Gray	TrD:255G→0G	TrD:255G→127G		...	TrR:255G→895G	TrR:255G→1023G
...	...	...	...		...	...
895Gray	TrD:895G→0G	TrD:895G→127G	TrD:895G→255G	...		TrR:895G→1023G
1023Gray	TrD:1023G→0G	TrD:1023G→127G	TrD:1023G→255G	...	TrD:1023G→895G	

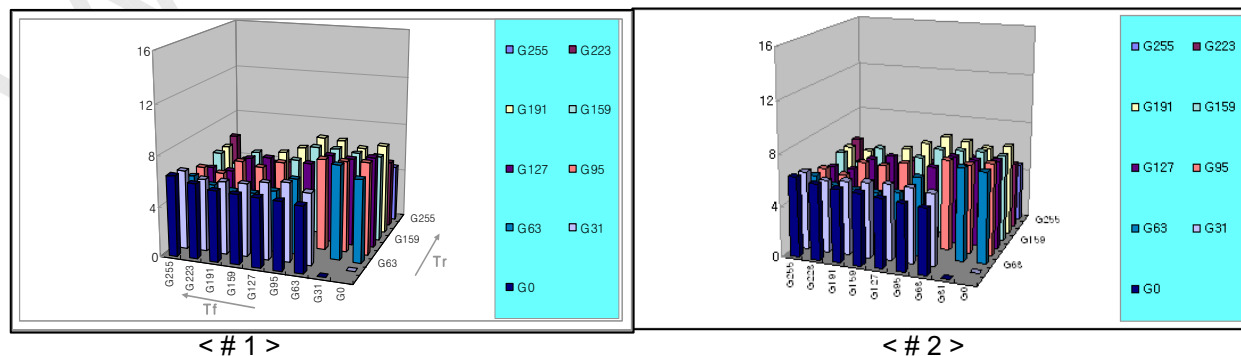
3. Sampling Size : 2 pcs

4. Measurement Method : Follow the same rule as optical characteristics measurement.

5. Current Status

Below table is actual data of production on **TBD** (L&T SVT Event Sample)

	G to G Response Time [ms]		Uniformity
	Min.	Max.	
# 1	TBD	TBD	TBD
# 2	TBD	TBD	TBD



LC470EUF

Product Specification

APPENDIX- VII-2

■ MPRT Response Time Uniformity (δ_{MPRT})

This is only the reference data of MPRT and uniformity for **LC470EUF-LDC6** model.

1. MPRT Response Time :

Response time is defined as Figure3

2. MPRT Uniformity

The variation of MPRT Uniformity , δ_{MPRT} is defined as :

$$\text{MPRT Uniformity} = \frac{\text{Maximum (MPRT)} - \text{Typical (MPRT)}}{\text{Typical (MPRT)}} \leq 1$$

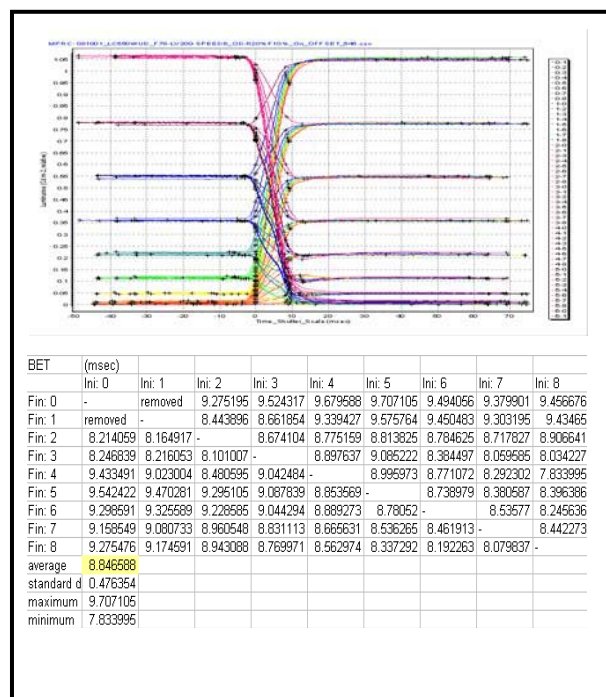
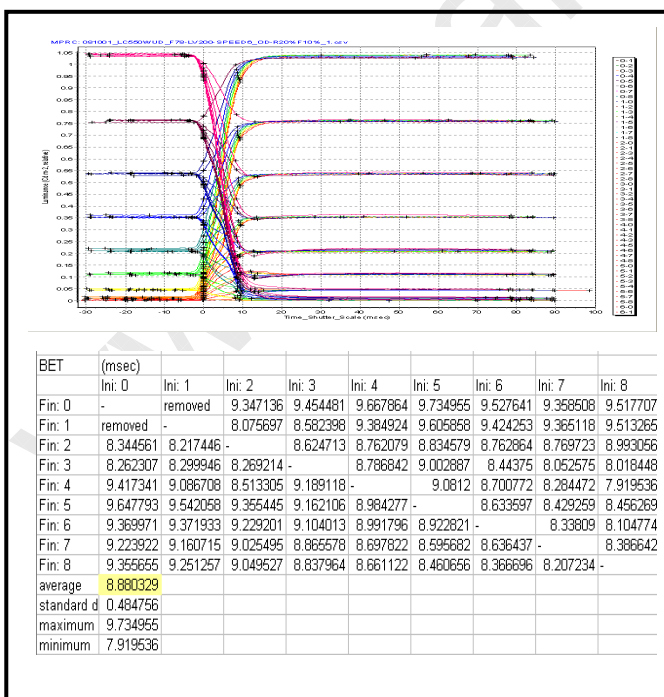
3. Sampling Size : 2 pcs

4. Measurement Method : Follow the same rule as optical characteristics measurement.

5. Current Status

Below table is actual data of production on **L&T SVT Event Sample**

Sample	MPRT Response Time [ms]		Uniformity
	Min.	Max.	
# 1	TBD	TBD	TBD
# 2	TBD	TBD	TBD



LC470EUF

Product Specification

APPENDIX- VIII

■ Standard specification of Eyeglasses

This is recommended data of Eyeglasses for **LC470EUF-LDC6** model. (details refer to table)

For each item, depending on the eyeglass manufacturer tolerances may occur, this tolerance can affect 3D performance. (3D Crosstalk, 3D luminance, 3D viewing angle)

<Table. Standard specification of Eyeglasses>

Design item of Eyeglasses		Left	Right	Remark
Optical axis	a) Slow axis of retarder	-45°	45°	Refer to drawing
	b) Transmission axis of polarizer	0°	0°	
Retardation value	Retarder	125nm		@550nm

※ Recommended polarizer

Polarization efficiency: more than 99.90%

